Reg.No.	

S.No. 190

BATCH: 2016, 2017

END OF SEMESTER EXAMINATIONS, APRIL / MAY -2018 ELECTIVE – II: EMBEDDED SYSTEM SUBJECT CODE: 12P3CS08

MAJOR: M.Sc.,(Computer Science)

TIME : 3 HOURS

SEMESTER : II MAX.MARKS: 70

SECTION - A (5 X 3 = 15)

Answer ALL Questions:

- 1. Define Display Units.
- 2. Describe IrDA.
- 3. Draw the diagram for null modem cable connection.
- 4. What is ZigBee?
- 5. Define cross complier?

SECTION - B (5 X 11 = 55)

Answer ALL Questions:

6. a) Give an overview of control systems and Industrial automation.

(OR)

- b) Describe in detail about memory organization.
- 7. a) Explain about the DSP Architecture.

(OR

- b) Briefly explain Kernal Architecture.
- 8. a) Discuss in detail about serial communication.

(OR)

- b) Explain serial communication with the 8051 Family of Macro-controller.
- 9. a) Briefly explain I/O types with examples.

(OR

- b) Discuss in detail about Internet Enabled System.
- 10.a) Explain about performance and performance Accelerators.

(OR)

b) Briefly explain Laboratory Tools.
